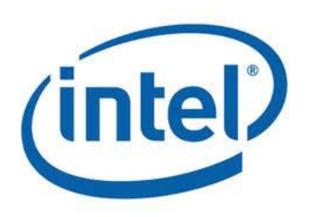


# Metal Contacts for the Hybrid Silicon Laser



Morgan Swaidan, Siddharth Jain, & John Bowers

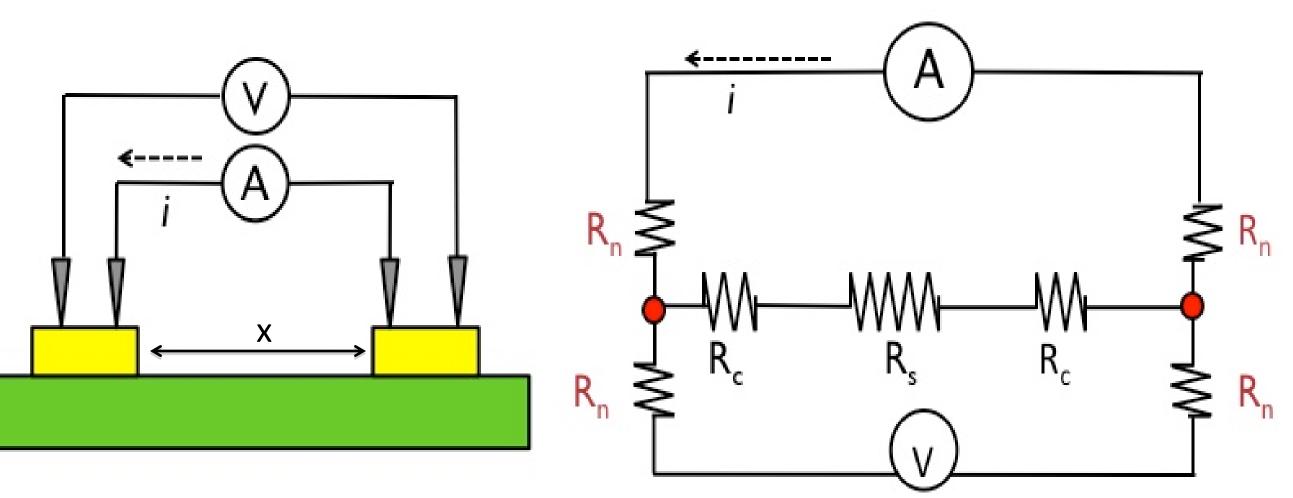
**Department of Electrical and Computer Engineering** 

### **Optical Communication**

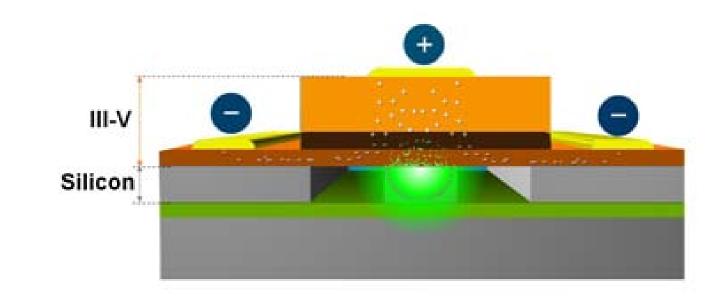
- Optical communication is today a niche technology used mainly for long distance communication (ex. Trans-Atlantic cables) because the cost of components is too high for widespread use.
- Fiber optic cables have at least 10 times the bandwidth of copper cables, which are overloaded by current communication rates.
- Our lab group wants to make this technology less expensive and adapt it for household use by making the light sources (lasers) for the cables cheaper.

## Methods

### Four Point Probe Measurement



## **Hybrid Silicon Laser**



- Laser made partially from expensive, light-producing III-V semiconductors Indium Phosphide (InP) and Indium Gallium Arsenide (InGaAs).
- Rest made of inexpensive Silicon brings down cost.
- Multiple metal contacts needed to allow current flow through laser.
- Problem : Conventional lasers made entirely from III-V use contacts containing gold, which is incompatible with Si manufacturing facilities.
- New, gold-free contacts must be designed and tested.

# **Metal Contacts**

#### Measured resistance is equal to $2R_c + R_s$ . Using the four point method eliminates error due to needle resistance ( $R_n$ )

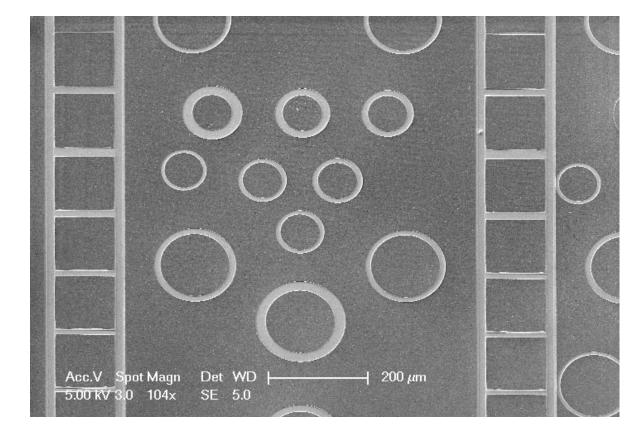
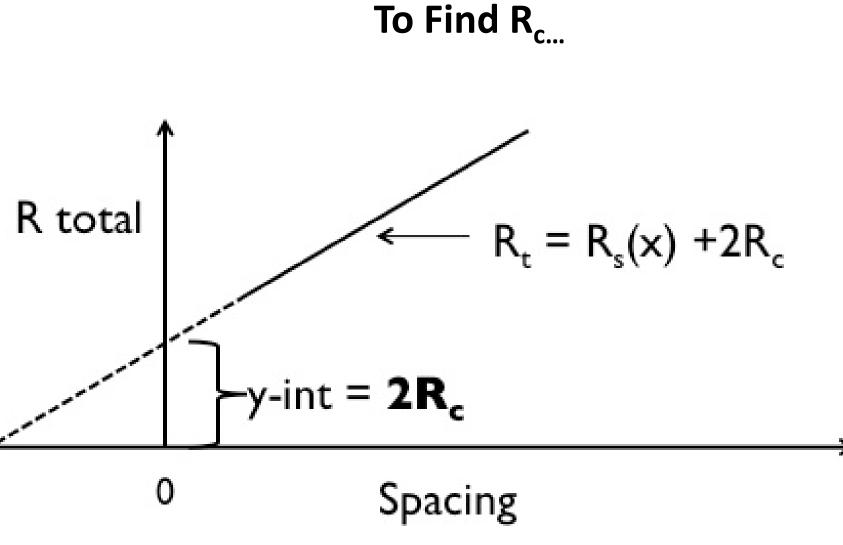


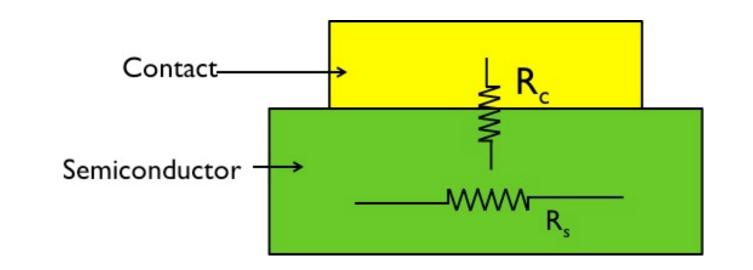
Photo courtesy Siddharth Jain



# Use test structures to measure total resistance (2Rc + Rs) across varied spacings

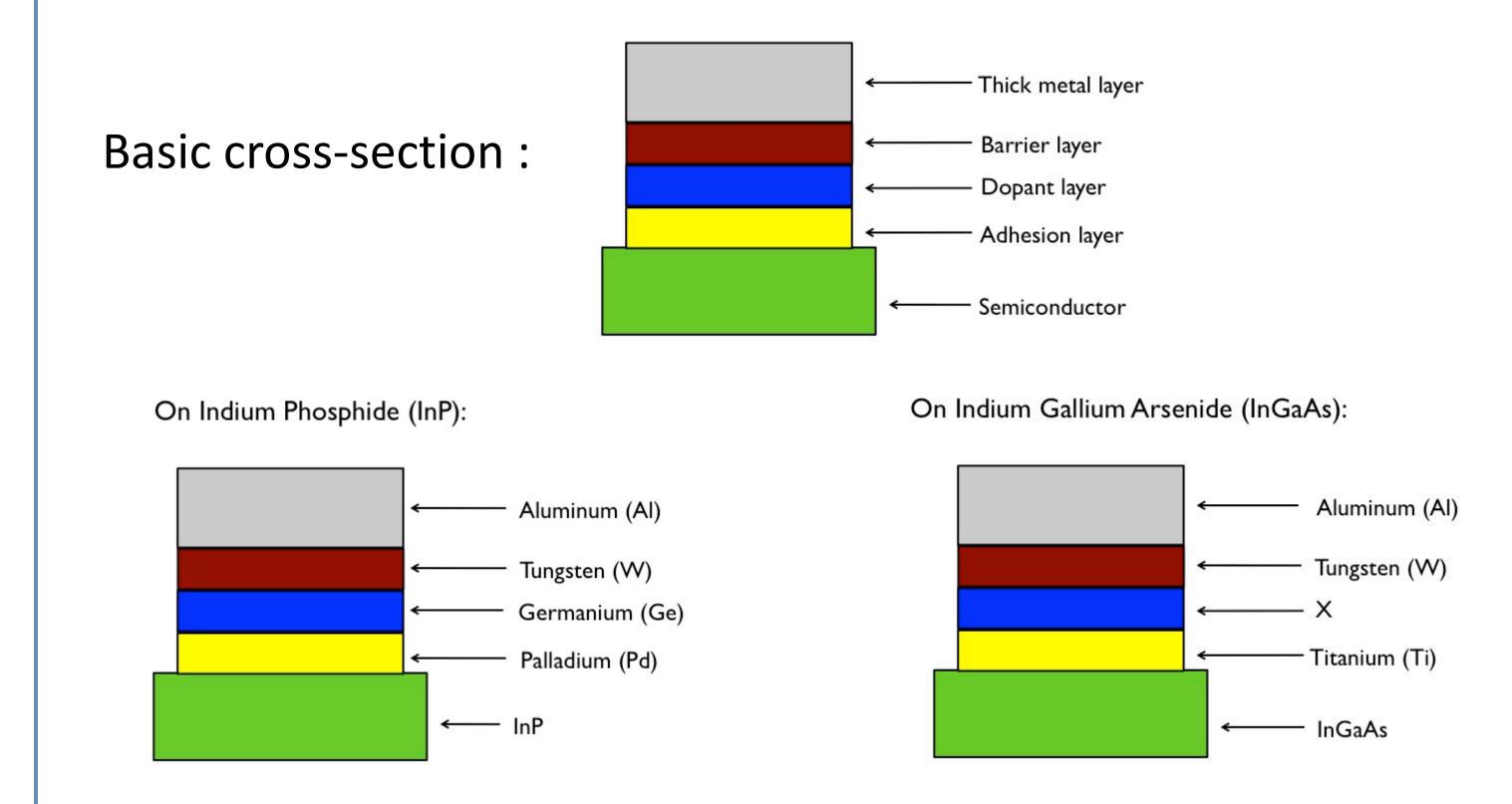
Both circular and square test structures are used – calculations are easier with squares, but circles tend to give more accurate data.

- $R_s$  is linearly dependent on the spacing between the contact larger space  $\rightarrow$  greater  $R_s$ .
- Extrapolating to find the yintercept (spacing = 0) gives 2R<sub>c,</sub> a constant.

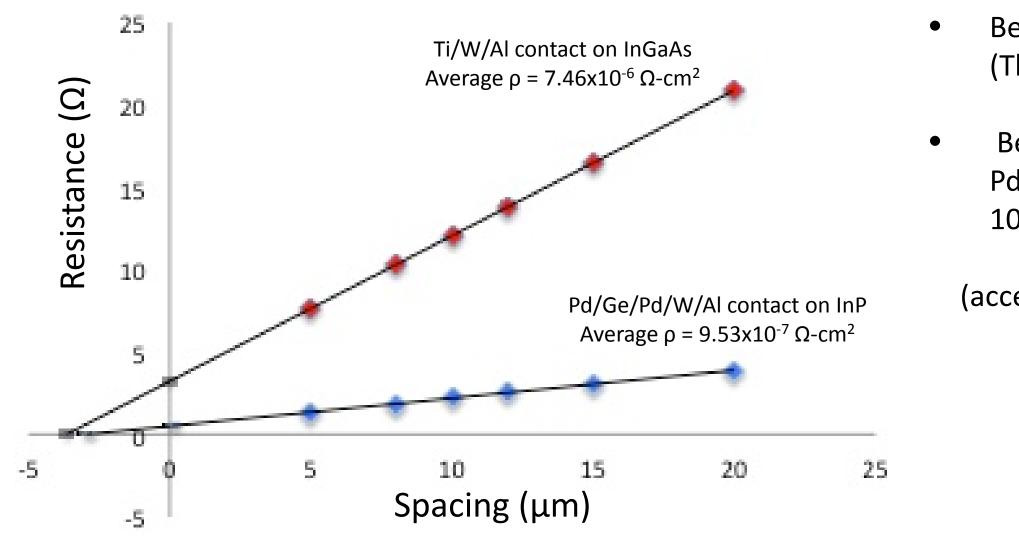


R<sub>c</sub>: Contact resistance, which we aim to minimize.
 -We record *specific* contact resistance (ρ) which takes into account the area through which the current flows.

**R**<sub>s</sub>: Inherent resistance of semiconductor.

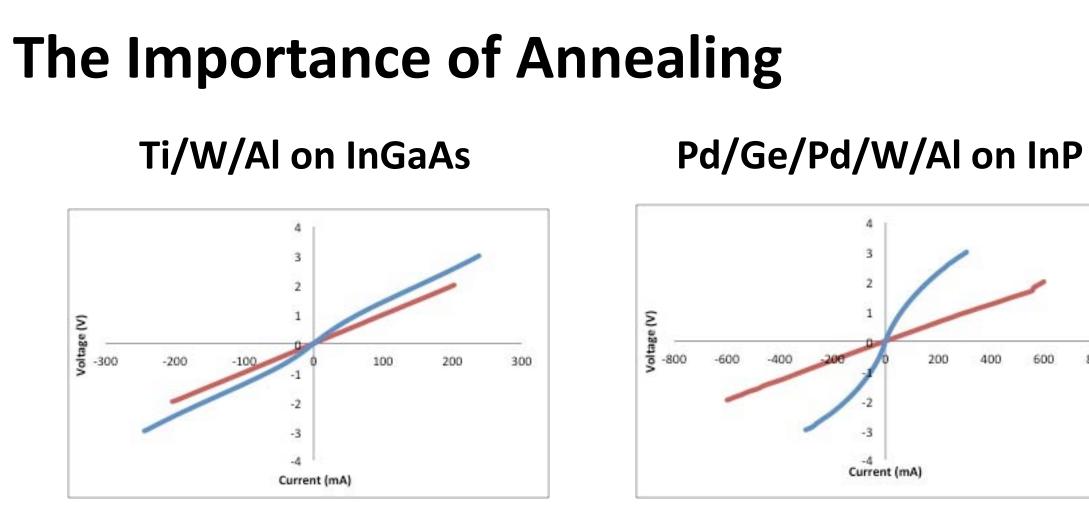


### **Selected Results**



- Best contact for InGaAs was Ti/W/Al (Thicknesses: 100nm/100nm/750nm)
- Best contact for InP was
  Pd/Ge/Pd/W/Al (Thicknesses:
  10nm/110nm/25nm/100nm/750nm)

(acceptable  $\rho$  values are anything less than  $10^{\text{-5}}\,\Omega\text{-cm}^2)$ 



Our model assumes that our samples obey Ohm's Law. To check if they actually do, we chart voltage vs. current. Ohmic samples should have a linear plot. These samples were Ohmic only after being annealed.

### Looking Ahead...

Find a contact that works equally well on both InGaAs and InP - Simplifies manufacturing

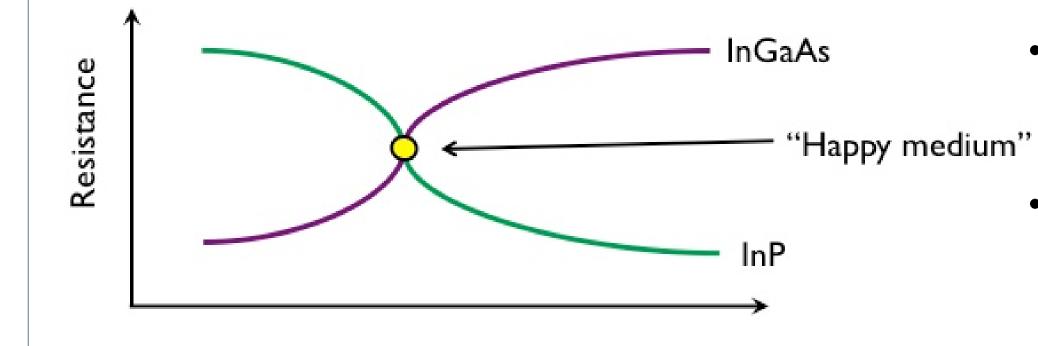
**Un-annealed** 

Annealed at

**350°C for 30s** 

### **Basic Resistance Trends**

**Amount of Ge in Contact** 



- Contacts with low Pd:Ge ratios tend to give relatively low resistances on InP.
- Contacts with high Pd:Ge ratios do better on InGaAs.
- Pd/Ge/W/Al contacts will be made with varying Pd:Ge ratios and tested on both InGaAs and InP.
- We'll see if ρ is acceptable at the ratio that gives the same resistance for both semiconductors.